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**LAB 5**

Q3. Sequential Multiplier

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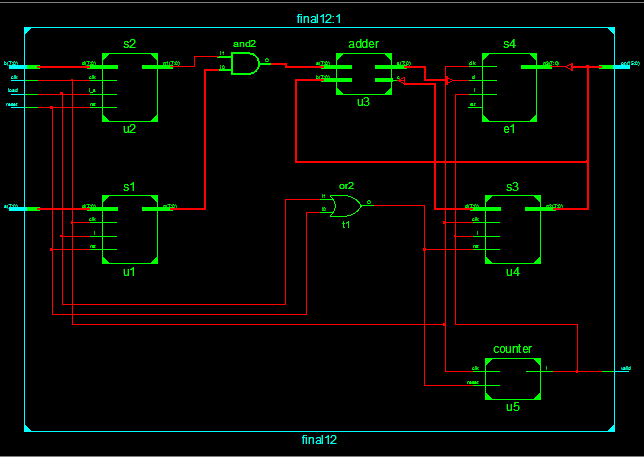
**Sequential Multiplier**

# Design Approach:

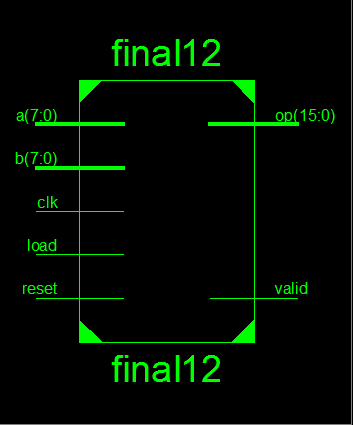
Sequential Multiplier is an electronic circuit used in digital electronics, to multiply two binary numbers. It is built using binary adder and other possible sequential circuits. A variety of computer arithmetic techniques can be used to implement a digital multiplier. Most techniques involve computing a set of partial products, and then summing the partial products together.

**Synthesis:**

1. RTL Schematic



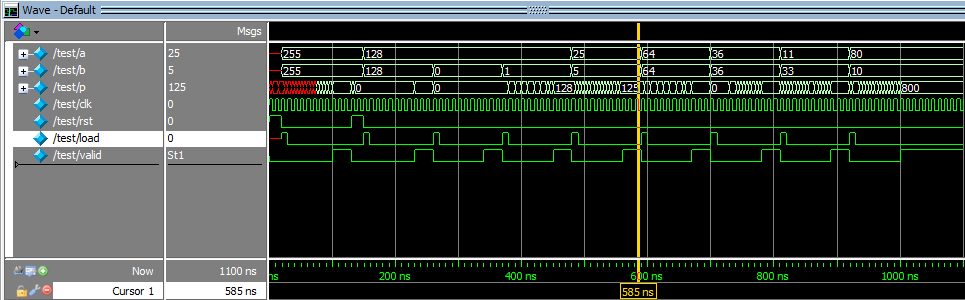
1. Block Diagram

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1. Tech Schematic



1. Simulation Waveform Result

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**Error:**

None

**Verified by:**

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